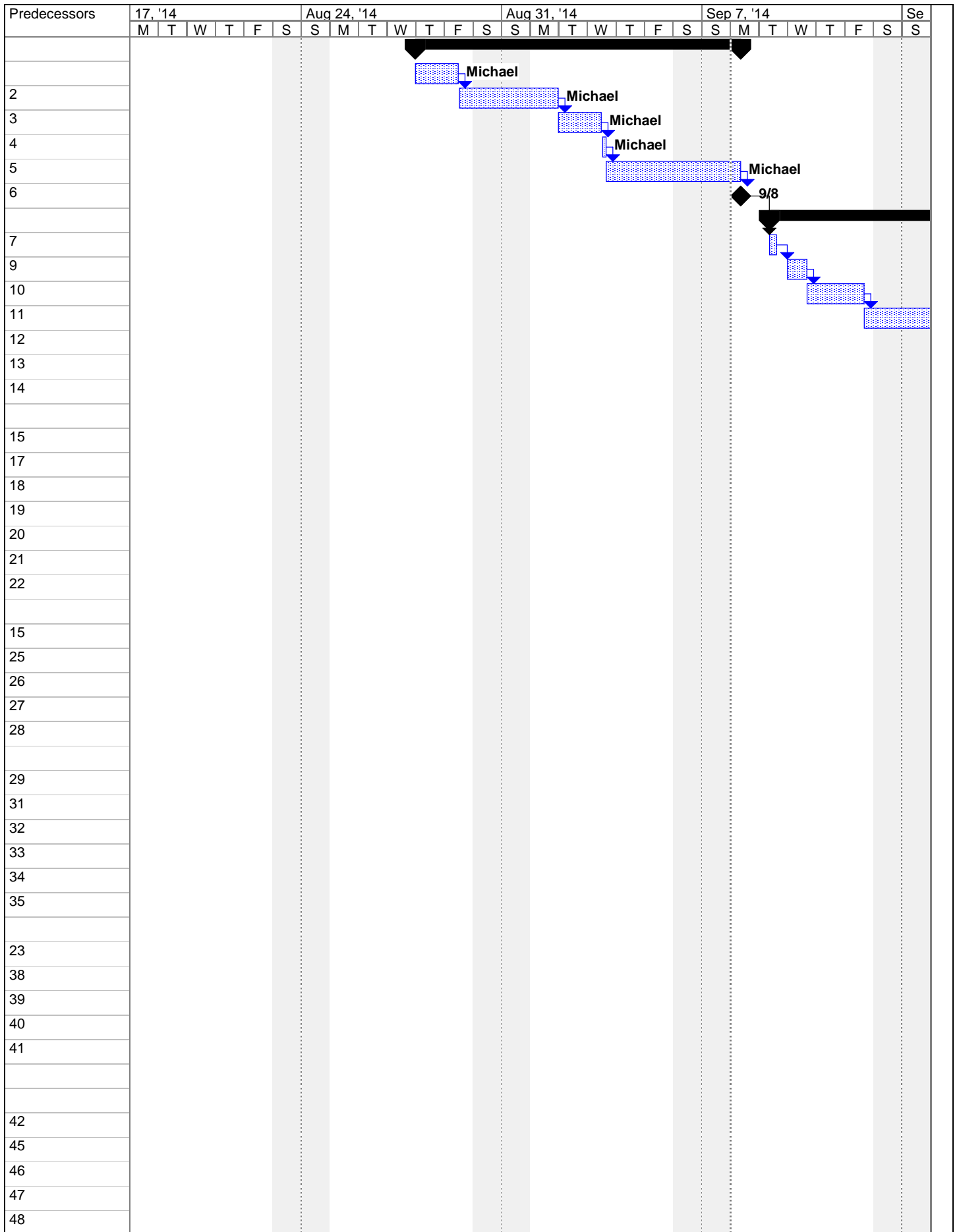
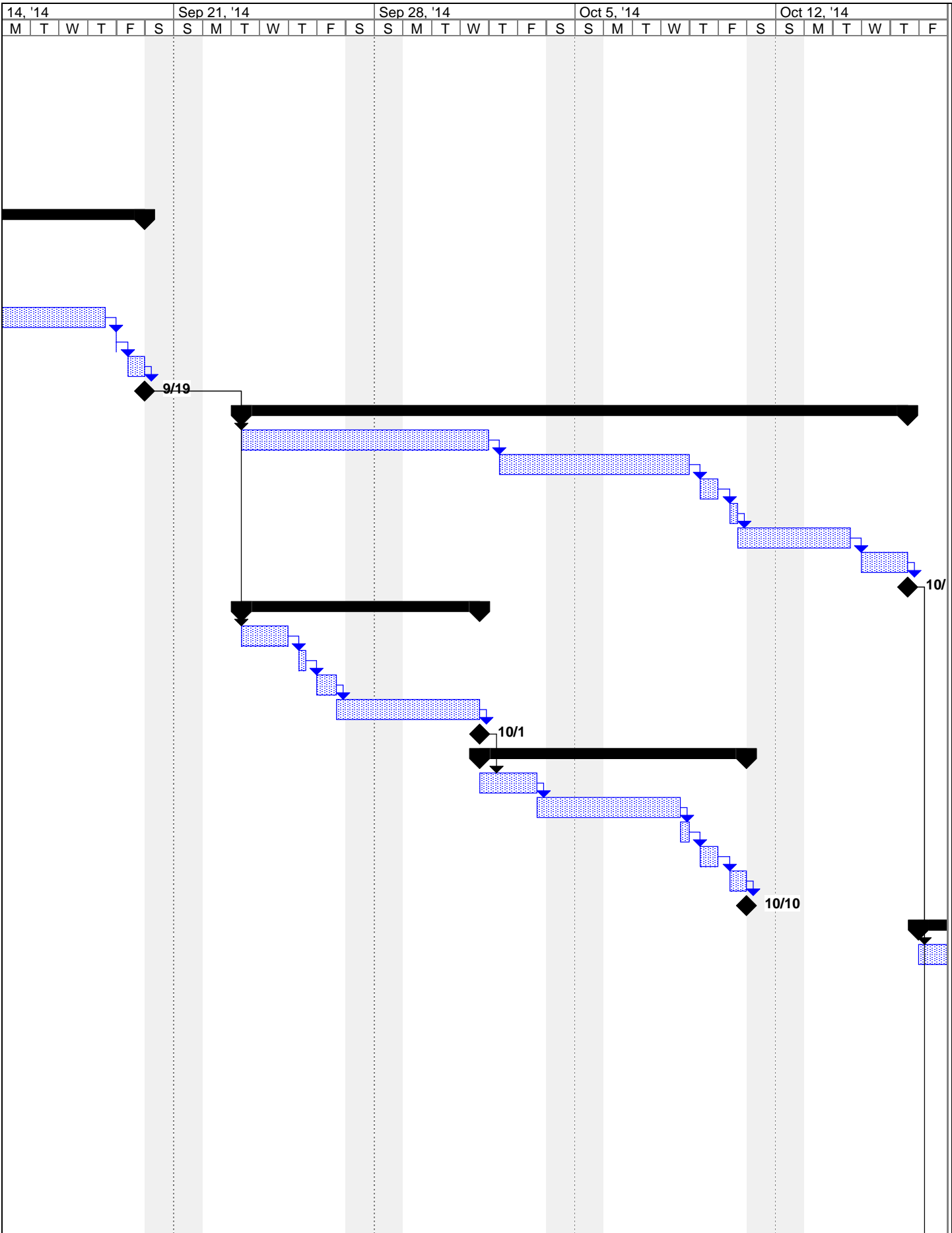


ID		Task Name	Duration	Start	Finish
1		<b>Group24 Project Scope</b>	<b>6 days</b>	<b>Thu 8/28/14</b>	<b>Mon 9/8/14</b>
2		Research Software Defined Radio	12 hrs	Thu 8/28/14	Fri 8/29/14
3		Research Mode-S Receivers	12 hrs	Fri 8/29/14	Mon 9/1/14
4		Research ADS-B Topics	12 hrs	Tue 9/2/14	Wed 9/3/14
5		Preliminary Costs Estimate	3 hrs	Wed 9/3/14	Wed 9/3/14
6		Develop Initial Project Plan	18 hrs	Wed 9/3/14	Mon 9/8/14
7		Scope complete	0 days	Mon 9/8/14	Mon 9/8/14
8		<b>Detailed Analysis</b>	<b>8 days</b>	<b>Tue 9/9/14</b>	<b>Fri 9/19/14</b>
9		Review specifications/budget with team	3 hrs	Tue 9/9/14	Tue 9/9/14
10		Incorporate feedback on specifications	6 hrs	Tue 9/9/14	Wed 9/10/14
11		Draft detailed hardware specifications	12 hrs	Wed 9/10/14	Fri 9/12/14
12		Draft detailed software specifications	18 hrs	Fri 9/12/14	Thu 9/18/14
13		Improve milestone timeline	3 hrs	Thu 9/18/14	Thu 9/18/14
14		Obtain approvals to proceed (concept, timeline, bu	6 hrs	Fri 9/19/14	Fri 9/19/14
15		Analysis complete	0 days	Fri 9/19/14	Fri 9/19/14
16		<b>Design Signal-to-USB</b>	<b>14.5 days</b>	<b>Tue 9/23/14</b>	<b>Thu 10/16/14</b>
17		Develop Schematics	36 hrs	Tue 9/23/14	Wed 10/1/14
18		Outline Verilog Approach	24 hrs	Thu 10/2/14	Wed 10/8/14
19		Outline Software Classes/Modules	6 hrs	Thu 10/9/14	Thu 10/9/14
20		Review specifications	3 hrs	Fri 10/10/14	Fri 10/10/14
21		Incorporate feedback into specifications	6 hrs	Fri 10/10/14	Tue 10/14/14
22		Revise and expand project documents	12 hrs	Tue 10/14/14	Thu 10/16/14
23		Design Signal-to-USB complete	0 days	Thu 10/16/14	Thu 10/16/14
24		<b>Design USB-to-Display</b>	<b>5.5 days</b>	<b>Tue 9/23/14</b>	<b>Wed 10/1/14</b>
25		Outline Software Classes/Modules	12 hrs	Tue 9/23/14	Wed 9/24/14
26		Review specifications	3 hrs	Thu 9/25/14	Thu 9/25/14
27		Incorporate feedback into specifications	6 hrs	Thu 9/25/14	Fri 9/26/14
28		Revise and expand project documents	12 hrs	Fri 9/26/14	Wed 10/1/14
29		Design USB-to-Display complete	0 days	Wed 10/1/14	Wed 10/1/14
30		<b>Design ADS-B Encryption</b>	<b>6.5 days</b>	<b>Wed 10/1/14</b>	<b>Fri 10/10/14</b>
31		Evaluate streaming vs block modes	12 hrs	Wed 10/1/14	Fri 10/3/14
32		Outline Software Classes/Modules	12 hrs	Fri 10/3/14	Wed 10/8/14
33		Review specifications	3 hrs	Wed 10/8/14	Wed 10/8/14
34		Incorporate feedback into specifications	6 hrs	Thu 10/9/14	Thu 10/9/14
35		Revise and expand project documents	6 hrs	Fri 10/10/14	Fri 10/10/14
36		Design ADS-B Encryption complete	0 days	Fri 10/10/14	Fri 10/10/14
37		<b>Prototype Signal-to-USB</b>	<b>14 days</b>	<b>Thu 10/16/14</b>	<b>Thu 11/13/14</b>
38		Breadboard solution	18 hrs	Thu 10/16/14	Wed 10/22/14
39		Develop Verilog Decoder	30 hrs	Wed 10/22/14	Fri 10/31/14
40		Developer testing (primary debugging)	24 hrs	Fri 10/31/14	Fri 11/7/14
41		Revise and expand project documents	12 hrs	Fri 11/7/14	Thu 11/13/14
42		Development Signal-to-USB complete	0 days	Thu 11/13/14	Thu 11/13/14
43		<b>Testing Signal-to-USB</b>	<b>12 days</b>	<b>Thu 11/13/14</b>	<b>Tue 1/13/15</b>
44		<b>Unit Testing</b>	<b>6 days</b>	<b>Thu 11/13/14</b>	<b>Wed 11/26/14</b>
45		Test component modules to product specifica	6 hrs	Thu 11/13/14	Tue 11/18/14
46		Identify anomalies to product specifications	6 hrs	Tue 11/18/14	Wed 11/19/14
47		Modify Verilog	6 hrs	Wed 11/19/14	Thu 11/20/14
48		Re-test modified code	6 hrs	Thu 11/20/14	Fri 11/21/14
49		Revise and expand project documents	12 hrs	Fri 11/21/14	Wed 11/26/14

ID	Task Name	Duration	Start	Finish
50	Unit testing Signal-to-USB complete	0 days	Wed 11/26/14	Wed 11/26/14
51	<b>Integration Testing</b>	<b>6 days</b>	<b>Wed 11/26/14</b>	<b>Tue 1/13/15</b>
52	Module integration	6 hrs	Wed 11/26/14	Thu 12/4/14
53	Identify anomalies to specifications	6 hrs	Thu 12/4/14	Tue 1/6/15
54	Modify code	6 hrs	Tue 1/6/15	Wed 1/7/15
55	Re-test modified code	6 hrs	Wed 1/7/15	Thu 1/8/15
56	Revise and expand project documents	12 hrs	Thu 1/8/15	Tue 1/13/15
57	Integration testing Signal-to-USB complete	0 days	Tue 1/13/15	Tue 1/13/15
58	<b>Develop USB-to-Display</b>	<b>10 days</b>	<b>Thu 10/16/14</b>	<b>Wed 11/5/14</b>
59	Coding	24 hrs	Thu 10/16/14	Thu 10/23/14
60	Developer testing (primary debugging)	24 hrs	Thu 10/23/14	Fri 10/31/14
61	Revise and expand project documents	12 hrs	Fri 10/31/14	Wed 11/5/14
62	Development USB-to-Display complete	0 days	Wed 11/5/14	Wed 11/5/14
63	<b>Testing USB-to-Display</b>	<b>11 days</b>	<b>Wed 11/5/14</b>	<b>Thu 12/4/14</b>
64	<b>Unit Testing</b>	<b>5 days</b>	<b>Wed 11/5/14</b>	<b>Tue 11/18/14</b>
65	Test component modules to product specifica	6 hrs	Wed 11/5/14	Thu 11/6/14
66	Modify code	6 hrs	Thu 11/6/14	Fri 11/7/14
67	Re-test modified code	6 hrs	Fri 11/7/14	Wed 11/12/14
68	Revise and expand project documents	12 hrs	Wed 11/12/14	Tue 11/18/14
69	Unit testing USB-to-Display complete	0 days	Tue 11/18/14	Tue 11/18/14
70	<b>Integration Testing</b>	<b>6 days</b>	<b>Tue 11/18/14</b>	<b>Thu 12/4/14</b>
71	Module integration	6 hrs	Tue 11/18/14	Wed 11/19/14
72	Identify anomalies to specifications	6 hrs	Wed 11/19/14	Thu 11/20/14
73	Modify code	6 hrs	Thu 11/20/14	Fri 11/21/14
74	Re-test modified code	6 hrs	Fri 11/21/14	Tue 11/25/14
75	Revise and expand project documents	12 hrs	Tue 11/25/14	Thu 12/4/14
76	Integration testing USB-to-Display complete	0 days	Thu 12/4/14	Thu 12/4/14
77	<b>Develop ADS-B Encryption</b>	<b>10 days</b>	<b>Wed 11/5/14</b>	<b>Wed 11/26/14</b>
78	Coding	24 hrs	Wed 11/5/14	Thu 11/13/14
79	Developer testing (primary debugging)	24 hrs	Thu 11/13/14	Fri 11/21/14
80	Revise and expand project documents	12 hrs	Fri 11/21/14	Wed 11/26/14
81	Development ADS-B Encryption complete	0 days	Wed 11/26/14	Wed 11/26/14
82	<b>Testing ADS-B Encryption</b>	<b>11 days</b>	<b>Wed 11/26/14</b>	<b>Wed 1/21/15</b>
83	<b>Unit Testing</b>	<b>5 days</b>	<b>Wed 11/26/14</b>	<b>Fri 1/9/15</b>
84	Test component modules to product specifica	6 hrs	Wed 11/26/14	Thu 12/4/14
85	Modify code	6 hrs	Thu 12/4/14	Tue 1/6/15
86	Re-test modified code	6 hrs	Tue 1/6/15	Wed 1/7/15
87	Revise and expand project documents	12 hrs	Wed 1/7/15	Fri 1/9/15
88	Unit testing ADS-B Encryption complete	0 days	Fri 1/9/15	Fri 1/9/15
89	<b>Integration Testing</b>	<b>6 days</b>	<b>Fri 1/9/15</b>	<b>Wed 1/21/15</b>
90	Module integration	6 hrs	Fri 1/9/15	Tue 1/13/15
91	Identify anomalies to specifications	6 hrs	Tue 1/13/15	Wed 1/14/15
92	Modify code	6 hrs	Wed 1/14/15	Thu 1/15/15
93	Re-test modified code	6 hrs	Thu 1/15/15	Fri 1/16/15
94	Revise and expand project documents	12 hrs	Fri 1/16/15	Wed 1/21/15
95	Integration testing ADS-B Encryption complet	0 days	Wed 1/21/15	Wed 1/21/15

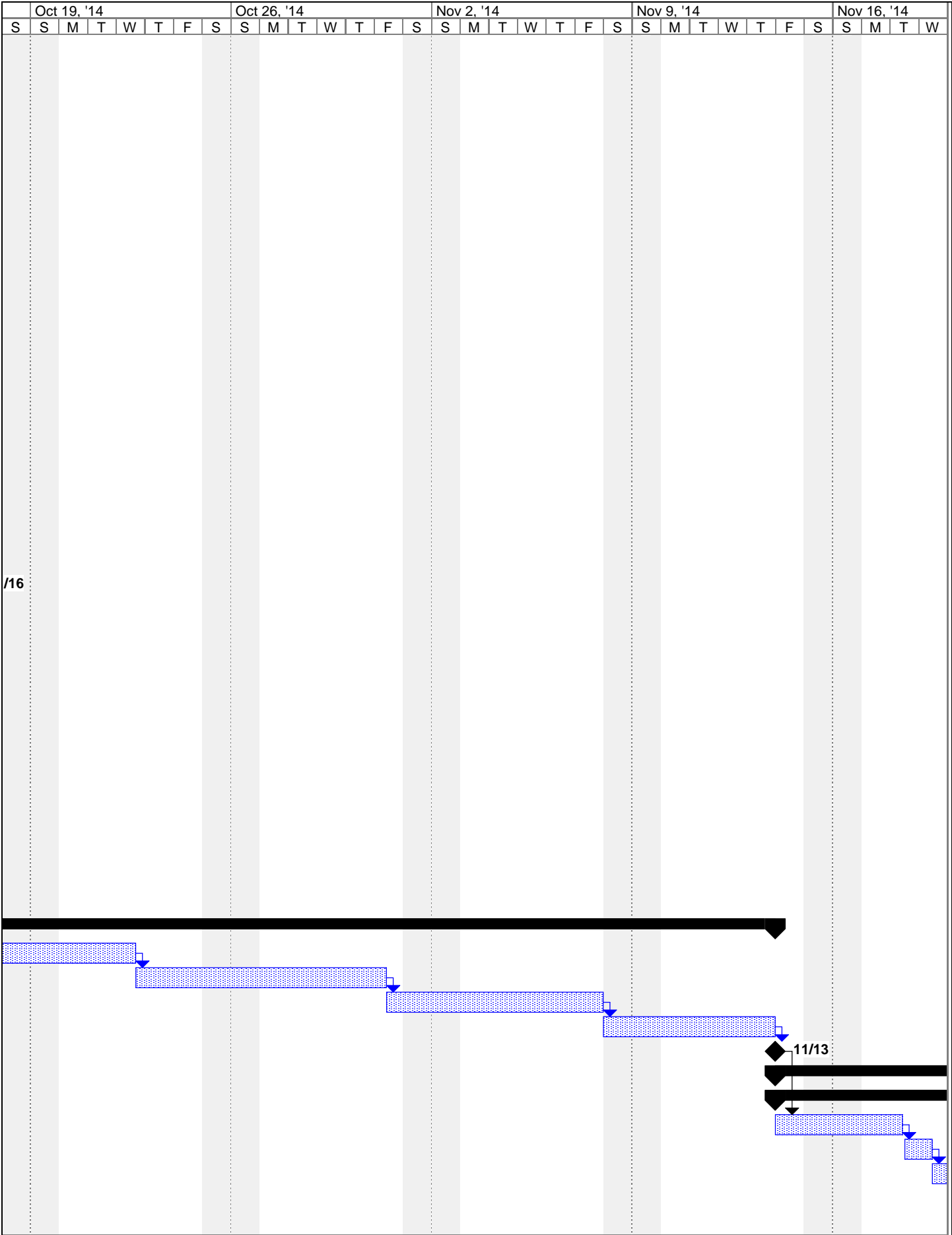


Predecessors	17, '14						Aug 24, '14						Aug 31, '14						Sep 7, '14						Se		
	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S
49																											
50																											
52																											
53																											
54																											
55																											
56																											
23																											
59																											
60																											
61																											
62																											
65																											
66																											
67																											
68																											
69																											
71																											
72																											
73																											
74																											
75																											
62																											
78																											
79																											
80																											
81																											
84																											
85																											
86																											
87																											
88																											
90																											
91																											
92																											
93																											
94																											



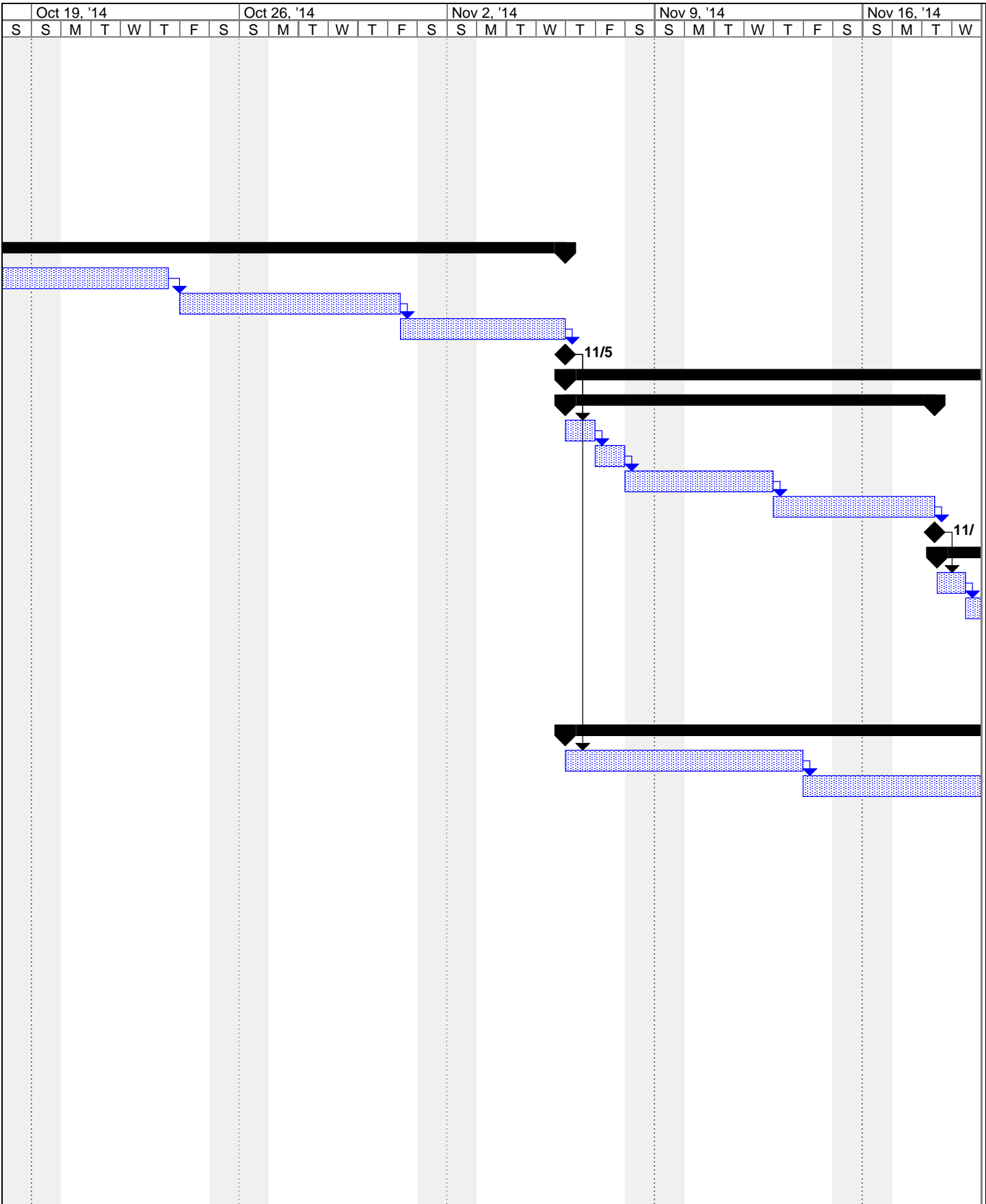
14, '14						Sep 21, '14						Sep 28, '14						Oct 5, '14						Oct 12, '14								
M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F





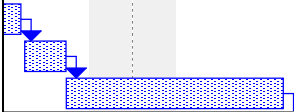
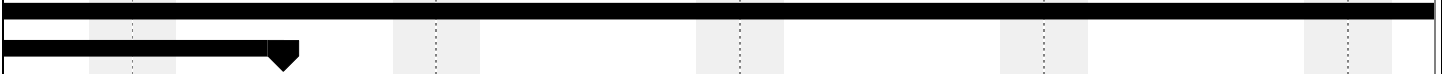
/16

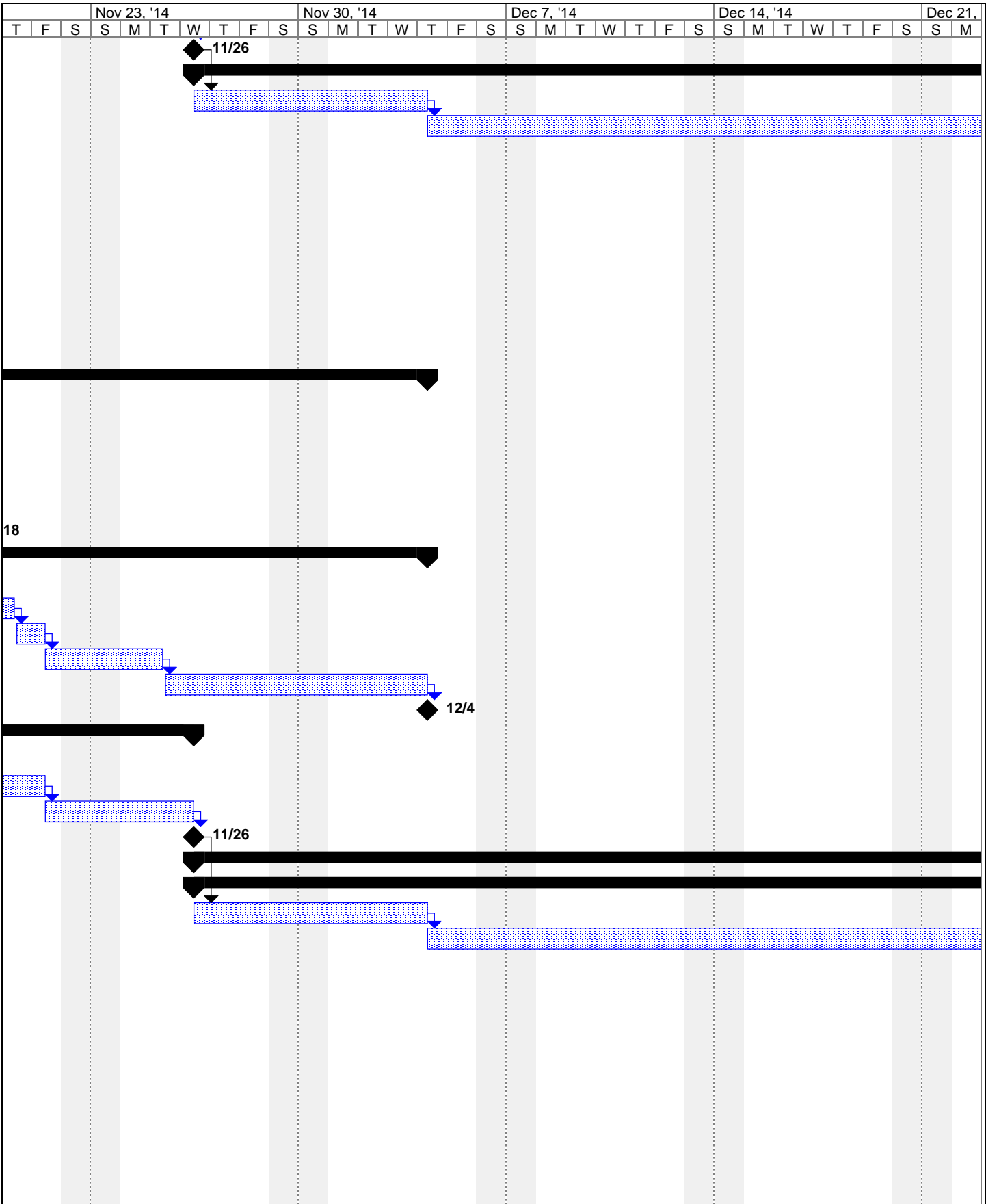
11/13





			Nov 23, '14							Nov 30, '14							Dec 7, '14							Dec 14, '14							Dec 21,	
T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M





14					Dec 28, '14					Jan 4, '14					Jan 11, '14					Jan 18, '14												
T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S	S	M	T	W	T	F	S



