CHAPTER 1

INTRODUCTION NUMBER SYSTEMS AND CONVERSION



This chapter in the book includes:

Objectives Study Guide

- Digital Systems and Switchi
- 1.1 Digital Systems and Switching Circuits
- 1.2 Number Systems and Conversion
- 1.3 Binary Arithmetic
- 1.4 Representation of Negative Numbers
- 1.5 Binary Codes Problems



Figure 1-1: Switching circuit

Decimal Notation $953.78_{10} = 9x10^2 + 5x10^1 + 3x10^0 + 7x10^{-1} + 8x10^{-2}$

Binary $1011.11_2 = 1x2^3 + 0x2^2 + 1x2^1 + 1x2^0 + 1x2^{-1} + 1x2^{-2}$ $= 8 + 0 + 2 + 1 + \frac{1}{2} + \frac{1}{4}$ $= 11.75_{10}$

EXAMPLE: Convert 53 ₁₀ to binary.										
2 / 53										
2 / 26	rem. = $1 = a_0$									
2 /13	rem. = $0 = a_1$									
2 / 6	rem. = $1 = a_2$	$53_{10} = 110101_2$								
2 /3	rem. = $0 = a_3$									
2 /1	rem. = $1 = a_4$									
0	rem. = $1 = a_5$									

Conversion (a)

EXAMPLE: Convert .625₁₀ to binary.

 $F = .625 \qquad F_1 = .250 \\ \times 2 \qquad \times 2 \\ \underline{1.250} \\ (a_{-1} = 1) \qquad (a_{-2} = 0)$

$$F_{2} = .500$$

$$\times 2 .625_{10} = .101_{2}$$

$$\frac{1.000}{(a_{-3} = 1)}$$

Conversion (b)

EXAMPLE: Convert 0.7₁₀ to binary.



Conversion (c)

EXAMPLE: Convert 231.3₄ to base 7.
231.3₄ = 2 × 16 + 3 × 4 + 1 +
$$\frac{3}{4}$$
 = 45.75₁₀
7 $\frac{/45}{/6}$ rem. 3 $\frac{7}{(5).25}$ 45.75₁₀ = 63.5151...7
 $\frac{7}{(1).75}$
 $\frac{7}{(5).25}$
 $\frac{7}{(1).75}$

Conversion (d)

Binary ⇔ Hexadecimal Conversion

$$1001101.010111_{2} = \frac{0100}{4} \quad \frac{1101}{D} \cdot \frac{0101}{5} \quad \frac{1100}{C} = 4D.5C_{16}$$

Equation (1-1)

Conversion from binary to hexadecimal (and conversely) can be done by inspection because each hexadecimal digit corresponds to exactly four binary digits (bits).

Add 13_{10} and 11_{10} in binary.

$11000 = 24_{10}$

Addition

The subtraction table for binary numbers is

$$0 - 0 = 0$$

 $0 - 1 = 1$ and borrow 1 from the next column
 $1 - 0 = 1$
 $1 - 1 = 0$

Borrowing 1 from a column is equivalent to subtracting 1 from that column.

Subtraction (a)

EXAMPLES OF BINARY SUBTRACTION:

(a)	1←	- (indicates	(b)	1111←	- borrows
	11101	a borrrow		10000	
-	-10011	from the	_	- 11	
-	1010	3rd column)	-	1101	

(c)
$$111 \leftarrow borrows$$

 111001
 -1011
 101110

Subtraction (b)

A detailed analysis of the borrowing process for this example, indicating first a borrow of 1 from column 1 and then a borrow of 1 from column 2, is as follows:

$$205 - 18 = [2 \times 10^{2} + 0 \times 10^{1} + 5 \times 10^{0}]$$

$$- [1 \times 10^{1} + 8 \times 10^{0}]$$
note borrow from column 1

$$= [2 \times 10^{2} + (0 - 1) \times 10^{1} + (10 + 5) \times 10^{0}]$$

$$- [1 \times 10^{1} + 8 \times 10^{0}]$$
note borrow from column 2

$$= [(2 - 1) \times 10^{2} + (10 + 0 - 1) \times 10^{1} + 15 \times 10^{0}]$$

$$- [1 \times 10^{2} + 8 \times 10^{1} + 7 \times 10^{0}] = 187$$

Subtraction (c)

The multiplication table for binary numbers is

0 x 0 = 0 0 x 1 = 0 1 x 0 = 0 1 x 1 = 1

Multiplication (a)

The following example illustrates multiplication of 13_{10} by 11_{10} in binary: 1101 1011 1101 1101 1101 $10001111 = 143_{10}$

Multiplication (b)

When doing binary multiplication, a common way to avoid carries greater than 1 is to add in the partial products one at a time as illustrated by the following example:

1111	multiplicand
<u>1101</u>	multiplier
1111	1st partial product
<u>0000</u>	2nd partial product
(01111)	sum of first two partial products
1111	3rd partial product
1001011)	sum after adding 3rd partial product
1111	4th partial product
1000011	final product (sum after adding 4th
	partial product)

Multiplication (c)

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Binary Division

Binary division is similar to decimal division, except it is much easier because the only two possible quotient digits are 0 and 1.

We start division by comparing the divisor with the upper bits of the dividend.

If we cannot subtract without getting a negative result, we move one place to the right and try again.

If we can subtract, we place a 1 for the quotient above the number we subtracted from and append the next dividend bit to the end of the difference and repeat this process with this modified difference until we run out of bits in the dividend.

The following example illustrates division of 145_{10} by 11_{10} in binary:



Binary Division

3 Systems for representing negative numbers in binary

Sign & Magnitude: Most significant bit is the sign $Ex: -5_{10} = 1101_2$

1's Complement: $N = (2^n - 1) - N$ Ex: $-5_{10} = (2^4 - 1) - 5 = 16 - 1 - 5 = 10_{10} = 1010_2$

2's Complement: $N^* = 2^n - N$ Ex: $-5_{10} = 2^4 - 5 = 16 - 5 = 11_{10} = 1011_2$

Section 1.4 (p. 16)

Table 1-1: Signed Binary Integers (word length n = 4)

	Positive		Negative Integers								
+N	Integers (all systems)	-N	Sign and Magnitude	2's Complement N*	1's Complement <i>N</i>						
+0	0000	-0	1000		1111						
+1	0001	-1	1001	1111	1110						
+2	0010	-2	1010	1110	1101						
+3	0011	-3	1011	1101	1100						
+4	0100	-4	1100	1100	1011						
+5	0101	-5	1101	1011	1010						
+6	0110	-6	1110	1010	1001						
+7	0111	-7	1111	1001	1000						
		-8		1000							

1. Addition of two positive numbers, sum $< 2^{n-1}$

+3	0011	
+4	0100	
+7	0111	(correct answer)

2. Addition of two positive numbers, sum $\geq 2^{n-1}$

+5	0101	
+6	0110	
	1011	← wrong answer because of overflow (+11 requires 5 bits including sign)

2's Complement Addition (a)

3. Addition of positive and negative numbers (negative number has greater magnitude)

+5	0101	
-6	1010	
-1	1111	(correct answer)

4. Same as case 3 except positive number has greater magnitude

-5	1011	
+6	0110	
+1	(1)0001	\leftarrow correct answer when the carry from the sign bit is ignored (this is <i>not</i> an overflow)
		is ignored (this is <i>not</i> an overflow)

2's Complement Addition (b)

5. Addition of two negative numbers, $|\text{sum}| \le 2^{n-1}$

$$\begin{array}{ccc} -3 & 1101 \\ -4 & 1100 \\ \hline -7 & (1)1001 \end{array} & \longleftarrow \text{ correct answer when the last carry is ignored} \\ & \text{ (this is not an overflow)} \end{array}$$

6. Addition of two negative numbers, $|sum| > 2^{n-1}$

$$\begin{array}{cccc}
-5 & 1011 \\
-6 & 1010 \\
\hline
(1)0101 & \longleftarrow \text{ wrong answer because of overflow} \\
& (-11 \text{ requires 5 bits including sign})
\end{array}$$

2's Complement Addition (c)

- **3.** Addition of positive and negative numbers (negative number with greater magnitude)
 - $\begin{array}{cccc}
 +5 & 0101 \\
 -6 & 1001 \\
 \hline
 -1 & 1110 & (correct answer)
 \end{array}$
- 4. Same as case 3 except positive number has greater magnitude

$$\begin{array}{cccc}
-5 & 1010 \\
\underline{+6} & 0110 \\
(1) & 0000 \\
\underline{-} & \underline{-} & 1 \\
0001 & (correct answer, no overflow)
\end{array}$$

1's Complement Addition (b)

5. Addition of two negative numbers, $|\operatorname{sum}| < 2^{n-1}$

$$\begin{array}{ccc} -3 & 1100 \\ \underline{-4} & \underline{1011} \\ (1) & 0111 \\ \underline{} & \underline{} & 1 \\ \underline{} & \underline{} & 1 \end{array} \quad (end-around carry) \\ \underline{} & 1000 \qquad (correct answer, no overflow) \end{array}$$

6. Addition of two negative numbers, $|\text{sum}| \ge 2^{n-1}$

$$\begin{array}{cccc}
-5 & 1010 \\
\underline{-6} & 1001 \\
(1) & 0011 \\
\underline{} & \underline{} & 1 \\
\end{array}$$
(end-around carry)
(wrong answer because of overflow)

1's Complement Addition (c)

1. Add -11 and -20 in 1's complement.

+11 = 00001011 +20 = 00010100

taking the bit-by-bit complement,

-11 is represented by 11110100 and -20 by 11101011

11110100	(-11)
11101011	+(-20)
(1) 11011111	
$1 \longrightarrow 1$	(end-around carry)
11100000 =	= -31

1's Complement Addition (d)

2. Add -8 and +19 in 2's complement

+ 8 = 00001000

complementing all bits to the left of the first 1, -8, is represented by 11111000

 $\begin{array}{r} 11111000 \quad (-8) \\ \underline{00010011} \quad +19 \\ \hline (4)00001011 = +11 \\ \frown \quad (discard last carry) \end{array}$

2's Complement Addition (d)

Binary Codes

Although most large computers work internally with binary numbers, the input-output equipment generally uses decimal numbers. Because most logic circuits only accept two-valued signals, the decimal numbers must be coded in terms of binary signals. In the simplest form of binary code, each decimal digit is replaced by its binary equivalent. For example, 937.25 is represented by:



Section 1.5 (p. 21)

Table 1–2. Binary Codes for Decimal Digits

Decimal Digit	8-4-2-1 Code (BCD)	6-3-1-1 Code	Excess-3 Code	2-out-of-5 Code	Gray Code
0	0000	0000	0011	00011	0000
1	0001	0001	0100	00101	0001
2	0010	0011	0101	00110	0011
3	0011	0100	0110	01001	0010
4	0100	0101	0111	01010	0110
5	0101	0111	1000	01100	1110
6	0110	1000	1001	10001	1010
7	0111	1001	1010	10010	1011

		1	٩SC	II C	ode	ć					ASC	II C	ode	ē		
Character	A_6	A_5	A_4	A_3	A_2	A_1	A_0	Character	A_6	A_5	A_4	A_3	A_2	A_1	A_0	
space	0	1	0	0	0	0	0	@	1	0	0	0	0	0	0	
!	0	1	0	0	0	0	1	А	1	0	0	0	0	0	1	
"	0	1	0	0	0	1	0	В	1	0	0	0	0	1	0	
#	0	1	0	0	0	1	1	С	1	0	0	0	0	1	1	
\$	0	1	0	0	1	0	0	D	1	0	0	0	1	0	0	
%	0	1	0	0	1	0	1	E	1	0	0	0	1	0	1	
&	0	1	0	0	1	1	0	F	1	0	0	0	1	1	0	
,	0	1	0	0	1	1	1	G	1	0	0	0	1	1	1	
(0	1	0	1	0	0	0	Н	1	0	0	1	0	0	0	
)	0	1	0	1	0	0	1	I	1	0	0	1	0	0	1	
*	0	1	0	1	0	1	0	J	1	0	0	1	0	1	0	Table 1-3
+	0	1	0	1	0	1	1	К	1	0	0	1	0	1	1	ASCII code
,	0	1	0	1	1	0	0	L	1	0	0	1	1	0	0	
_	0	1	0	1	1	0	1	M	1	0	0	1	1	0	1	(incomplete)
	0	1	0	1	1	1	0	N	1	0	0	1	1	1	0	
/	0	1	0	1	1	1	1	0	1	0	0	1	1	1	1	
0	0	1	1	0	0	0	0	Р	1	0	1	0	0	0	0	
1	0	1	1	0	0	0	1	Q	1	0	1	0	0	0	1	
2	0	1	1	0	0	1	0	R	1	0	1	0	0	1	0	
3	0	1	1	0	0	1	1	S	1	0	1	0	0	1	1	
4	0	1	1	0	1	0	0	Т	1	0	1	0	1	0	0	