Problem 1. Neamen 16.3

Problem 2. Neamen 16.49

Problem 3. Neamen 16.60

Problem 4. Design CMOS logic gates for the following functions:

1. \( Y = \overline{ABCD} \)
2. \( Y = A + B + C + D \)
3. \( Y = ABC + D \)
4. \( Y = (AB + C)D \)